

FIG. 1

000260-20286260

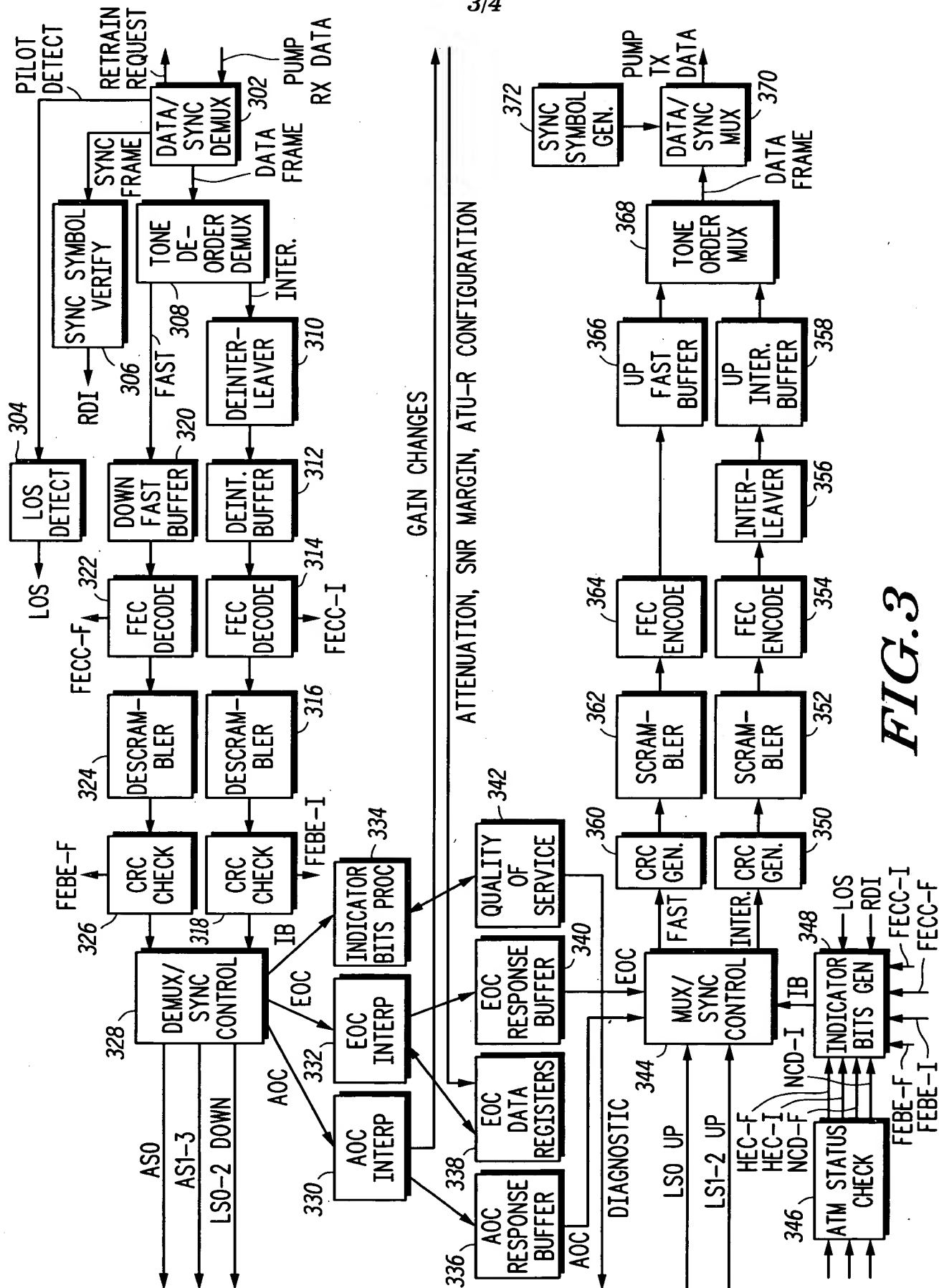


FIG.3

400

	HOST	DSP	
0-T	SENDS SYMBOLS A-C IN A BURST OVER PCI BUS	QUEUES A, B, C IN INCOMING DATA BUFFER	<div>402</div> <div>C</div> <div>B</div> <div>A</div>
T-2T	X	QUEUES A, B, C IN INCOMING DATA BUFFER	<div>404</div> <div>X</div> <div>C</div> <div>B</div>
2T-3T	X	QUEUES A, B, C IN INCOMING DATA BUFFER	<div>406</div> <div>X</div> <div>X</div> <div>C</div>
3T-4T	SEND SYMBOL D	QUEUES A, B, C IN INCOMING DATA BUFFER	<div>408</div> <div>X</div> <div>X</div> <div>D</div>
4T-5T	X	SEND SYMBOL D	<div>410</div> <div>X</div> <div>X</div> <div>X</div>
5T-6T	X	RESEND SYMBOL D, INCREMENT COUNTER TO 1	<div>412</div> <div>X</div> <div>X</div> <div>X</div>
6T-7T	SEND SYMBOLS E, F, G	RESEND SYMBOL D, QUEUE SYMBOLS E, F, G, INCREMENT COUNTER TO 2	<div>414</div> <div>G</div> <div>F</div> <div>E</div>
7T-8T	SEND SYMBOLS H, I	THROW AWAY E AND F BASED ON COUNTER AND SEND SYMBOL G, QUEUE SYMBOLS H AND G	<div>416</div> <div></div> <div>I</div> <div>H</div>

FIG. 4